		Hits	Search Text	DB	Time stamp	
L	ımber	HICS		USPAT;	2003/05/17	
1	Imber	3160819	carrier substrate pcb board	US-PGPUB;	12:53	1
				EPO; JPO;		ĺ
				DERWENT; IBM TDB		
			(split hole via passage opening) with	USPAT;	2003/05/17	
2		459543	(carrier substrate pcb board)	US-PGPUB;	12:55	
			(0011111	EPO; JPO; DERWENT;		
İ				IBM_TDB		
		94443	(semiconductor die chip dice ic	USPAT;	2003/05/17	
3		21112	(SPIII) same ((SPIII)	US-PGPUB; EPO; JPO;	12.57	
		;	hole via passage opening) with (carrier substrate pcb board))	DERWENT;		1
]			IBM_TDB USPAT;	2003/05/17	١.
4		9182	wires same ((semiconductor die chip dice ic (integrated adj circuit)) same ((split	US-PGPUB;	12:58	
			hole via passage opening) with (carrier	EPO; JPO;		
1			substrate pcb board)))	DERWENT; IBM TDB		
			(wires same ((semiconductor die chip	USPAT;	2003/05/17	
5	ļ	3207	l ii - i /integrated adl Clrcult// Same	US-PGPUB;	12:58	
			l //cmlit hole via passage opening/ with	EPO; JPO; DERWENT;		
			((split hole via published)))) and (bump ball)	IBM_TDB	222 /25 /27	
16	•	2072	//wires same ((semiconductor die chip	USPAT; US-PGPUB;	2003/05/17	
		20.2	lains is /integrated add circuit)) same	EPO; JPO;	15.01	
			((split hole via passage opening) with (carrier substrate pcb board))) and	DERWENT;		
			(bump ball)) and (resin sealing	IBM_TDB		
y.			encapsulating)	USPAT	2003/05/17	
-	7	1		**************************************	13:39 2003/05/17	
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	-	,		USPAT	2003/05/17	ł
	9	1		USPAT	13:39 2003/05/17	
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Ì		1		USPAT	2003/05/17	ł
	11			USPAT	2003/05/17	
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	13	1		USPAT	2003/05/17	1
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	15	1		USPAT	13:40	
				USPAT	2003/05/17	}
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	10			USPAT	2003/05/17	
	18			USPAT	2003/05/17	
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	20		1	USPAT	2003/05/17	
	20			USPAT	2003/05/17	
	21		1	· · · · · · · · · · · · · · · · · · ·	13:41 2003/05/17	
	22		1	USPAT	13:41	
				USPAT	2003/05/17	
	23		1	USPAT	13:41 2003/05/17	
	24		1	OSEAT	13:41	
ļ			1	USPAT	2003/05/17	
	25		1		13:41	

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